

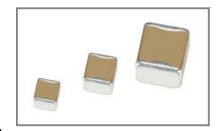
■ Microwave MLCC (RF Series)

♦ Feature

- * There is high reliability on monolithic structure of laminated layers.
- * And its character of excellent soldering ability and soldering resistance ability is suitable for reflow soldering and peak soldering.
- * It includes high and stable capacitance.
- * High Q
- * Low equivalent series resistance
- * High self-resonant frequency
- * Executive Standard: GB/T 21041-2007

Application

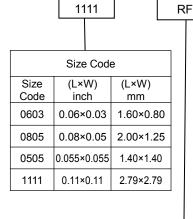
- * Mobile communication base station
- * Wireless communication products
- * RF power amplifier
- * Impedance matching network
- * Filter network
- * VCO

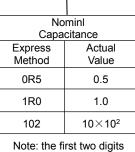


Ν



♦How To Order





5R0

В

are significant; third digit denotes number of zeros; R=decimal point.

Rated	d Voltage
(ur	nit): V
Express Method	Actual Value
6R3	6.3
500	50×10°
201	20×10¹

500

Note: the first two digitsare significant; third digit denotes number of zeros; R=decimal point.

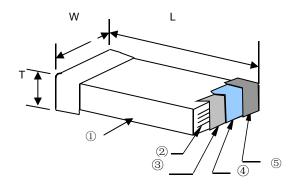
Package Styles					
Express Method	Package Styles				
В	Bulk Bag				
Т	Taping Package				

Dielectric Code					
Dielectric Code	Dielectric				
RF	C0G				

	Capacitance Tolerance						
Code	Tolerance	Note					
Α	±0.05pF						
В	±0.10pF	These Capacitance					
С	±0.25pF	tolerance A, B, C, D					
D	±0.50pF	are just applicable the					
F	±1%	capacitance that equals to or less than					
G	±2%	10pF。					
J	±5%	Topi »					

Terminal Material Styles				
Termination	Express			
Styles	Method			
Nickel				
Barrier N				
Termination				

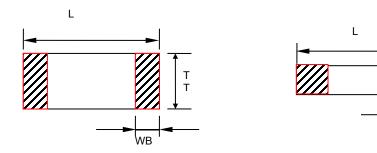
♦Product Structure



NO	Name			
1)	Ceramic dielectric			
2	Inner electrode			
3	Substrate electrode			
4)	Nickel Layer			
(5)	Tin Layer			



♦ Product Dimensions



Ту	уре	Dimensions (mm)			
British expression	Metric expression	L	w	Т	WB
0603	1608	1.60±0.10	0.80±0.10	0.80±0.10	0.35±0.20
0805	2012	2.00±0.20	1.25±0.20	0.80±0.20 1.25±0.20	0.50±0.20
0505	1414	1.40±0.38	1.40±0.38	≤1.45	0.30±0.10
1111	2828	2.79±0.50	2.79±0.50	≤2.59	0.80±0.30

Note: We can design according to customer special requirements

♦ Temperature Coefficient /Characteristics

介质种类 参考温度点		标称温度系数	工作温度范围	
Dielectric Reference Temperature Point		Temperature Coefficient	Operation Temperature Range	
COG	20°C	0 ±30ppm/℃		

♦ Capacitance Range and Operating Voltage

Dielectric	COG						
Dimension	0603	0805	0505	1111			
Voltage	25V~250V	25V~250V	25V~250V	≤50V	100V	200V/ 250V	500V
Capacitance							
0.3pF							
0.4pF							
0.5pF							
0.6 pF							
0.7pF							
1.0pF							
1.2pF							
1.5pF							
1.8pF							
2.0pF							
2.2pF							
3.3pF							
3.9pF							
4.7pF							
5.6pF							
6.8pF							
8.2pF							



Dielectric	COG						
Dimension	0603	0805	0505	1111			
Voltage	25V~250V	25V~250V	25V~250V	≤50V	100V	200V/ 250V	500V
Capacitance							
10pF							
12pF							
15pF							
18pF							
22pF							
27pF							
33pF							
39pF							
47pF							
56pF							
68pF							
82pF							
100pF							
120pF							
150pF							
180pF							
220pF							
270pF							
330pF							
390pF							
470pF							
560pF							
680pF							
820pF							
1000pF							

Note: 1、 Normal production 2、 We can design according to the customer requirements.

♦ Reliability Test

Item	Technical Specification	Test Method and Remarks			
		Capacitance	Measuring Frequency	Measuring Voltage	
Capacitance	Should be within the specified tolerance.	≤1000pF	1MHz±10%	4.0.0.01	
		>1000 pF	1KHz±10%	1.0±0.2Vrms	
Q	C≥30pF, Q≥1000 C<30pF, Q≥400+20C	Test Frequency: 1MHz±10% Test Voltage: 1.0±0.2Vrms			



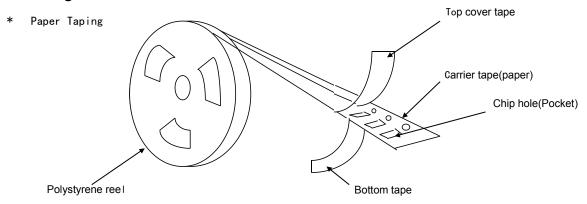
Item		Technical Specification	Test Method and Remarks			
(IR) Insulation Resistance	≥10,000MΩ		Measuring Voltage: Rated Voltage (Max 500V) Duration: 60±5s Test Humidity: ≤75% Test Temperature: 25°C±3°C Test Current: ≤50mA			
(DW) Dielectric Withstanding Voltage	No breakdo	own or damage.	Measuring Voltage: Class I :300% Rated voltage Duration: 1~5s Charge/ Discharge Current: 50mA max.			
Solderability	new solder	% of the terminal electrode is covered by . earance: No visible damage.	Preheating conditions:80 to 120 Solder Temperature: 235±5℃ Duration: 2±0.5s	°C; 10~30s. Solder Temperature: 245±5°C Duration: 2±0.5s		
	ΔC/C	${\leqslant}{\pm}2.5\%$ or ${\pm}0.25\text{PF},$ whichever is larger	Preheating conditions: 100 to 200°C; 60−120s.			
Resistance to Soldering	Q	Same to initial value.	Solder Temperature: 265±5℃ Duration: 10±1s Clean the capacitor with solvent and examine it with a			
Heat		Same to initial value. e: No visible damage.At least 95% of the ectrode is covered by new solder.	10X(min.) microscope. Recovery Time: 24±2h Recovery condition: Room temperature			
Appearance: No visible damage. Resistance to Flexure of Substrate		Test Board: PCB Warp: 1m Speed: 1mm/sec. U The measurement should be bending position.	nit: mm			
(Bending Strength)	$\Delta \text{C/C}$ Within ±5% or ±0.5pF , whichever is larger		1mm 45±2 45±2			
Termination Adhesion	No visible o	damage.	Applied Force: 5N Duration:	10±1S		



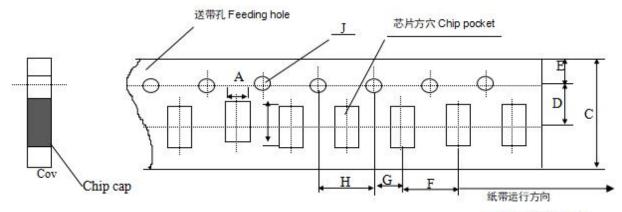
ltem		Technical Specification		Test Method and Remarks				
	Item COG			Initial Measurement				
				Cycling Times: 5 times, 1 cycle, 4 steps:				
	ΔC/C			Step 1	(Temperature) (°C) (Low- category temp.):	时间(Time) 30min		
	Q			2	(C0G:-55) (Normal temp.) : +20°C	2∼3min		
Temperature Cycle	IR	Same to initial value.		3	(Up- category temp.) (C0G: +125)	30min		
				4	(Normal temp.): +20°C	2∼3min		
	No visible damage.			试验后放置(恢复)时间: 24±2h Recovery time after test: 24±2h				
	Class I: ±7.5% or ±0.75pF, whichever is larger.							
Humidity load	Q	Q Not more than twice of initial value.			Temperature: 40±2°C Humidity: 90~95%RH Voltage: Rated Voltage Duration: 500h Recovery conditions: Room temperature Recovery Time: 24h			
	IR	IR Ri≥1000MΩ						
	Appearance: No visible damage.							
	Δ C/C COG \pm 2% or \pm 1pF, whichever is larger.							
	Q	Not more than twice of initial value.			Voltage:			
Life Test	Ri≥4000MΩ or Ri•C _R ≥40S whichever is smaller.			Rated Voltage<100V: 2Ur 100V≤Rated Voltage≤200V: 1.5 Ur 200V <rated 1.3="" 1000h="" 125°c<="" duration:="" td="" temperature:="" ur="" voltage≤500v:=""></rated>				
	Appearance: No visible damage.			Charge/ Discharge Current: 50mA max. Recovery Conditions: Room Temperature Recovery Time: 24h				



◆Package



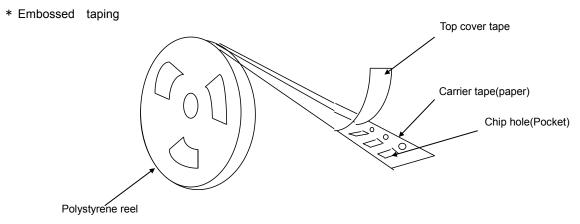
st Dimensions of paper taping for 0603, 0805 types.



Tape running direction

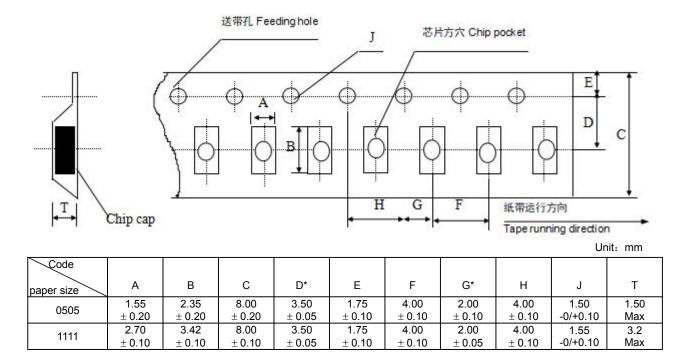
									Uni	t: mm
Code										
paper size	Α	В	С	D*	E	F	G*	Н	J	T
0603	1.10	1.90	8.00	3.50	1.75	4.00	2.00	4.00	1.50	1.10
0003	± 0.10	± 0.10	± 0.10	± 0.05	± 0.10	± 0.10	± 0.10	± 0.10	-0/+0.10	Max
0905	1.45	2.30	8.0	3.50	1.75	4.00	2.00	4.00	1.50	1.10
0805	± 0.15	± 0.15	± 0.15	± 0.05	± 0.10	± 0.10	± 0.10	± 0.10	-0/+0.10	Max

Note: The place with "*" means where needs exactly dimensions.



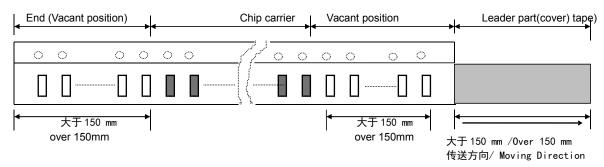
* Dimensions of embossed taping for 0505, 1111 type



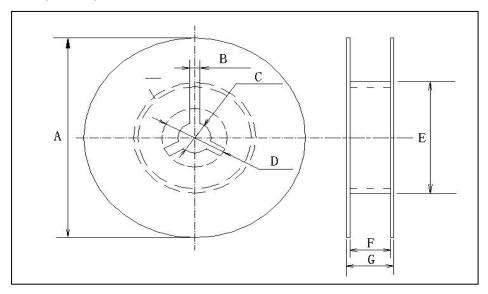


Note: The place with "*" means where needs exactly dimensions.

* Structure of leader part and end part of the carrier paper



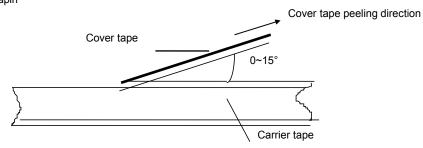
Reel dimensions (unit: mm)



卷盘型号	Α	В	С	D	Е	F	G
7'REEL	φ178±2.0	3.0	φ13±0.5	φ21±0.8	φ50 or more	10.0±1.5	12max

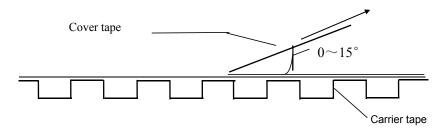


* Taping specification: top tape peeling strength Paper Tapin



* Embossed Taping

Cover tape peeling direction



Standard: 0.1N < peeling strength < 0.7N

No paper dirty remains on the scotch when peeling, and sticks to top and bottom tape.

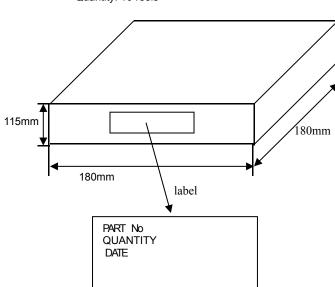
* Packing Quantity

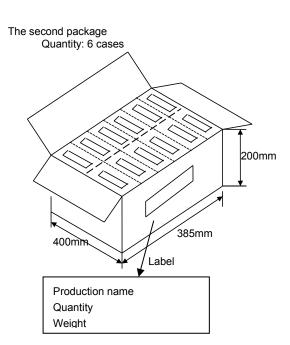
尺寸	Package Style & Quantity unit: pcs						
(SIZE)	EPT	PT	ET	ВС	BP		
0603		4000		15000	5000		
0805		4000	3000	10000	5000		
0505			500				
1111			500				

Note: We can choose packing style and quantity can be according to the customer's requirement.

* Outer packing

The first package Quantity: 10 reels







♦Storage Methods

* The guaranteed period for solderability is 12 months (Under deliver package condition).

* Storage conditions: Temperature $5\sim40^{\circ}$ C Relative Humidity $20\sim70\%$

♦Precautions For Use

The Multi-layer Ceramic Capacitors (MLCC) may fail in a short circuit modern in an open circuit mode when subjected to severe conditions of electrical environment and / or mechanical stress beyond the specified "rating" and specified "conditions" in the specification, which will result in burn out, flaming or glowing in the worst case. Following "precautions for "safety" and Application Notes shall be taken in your major consideration. If you have a question about the precautions for handling, please contact our engineering section or factory.

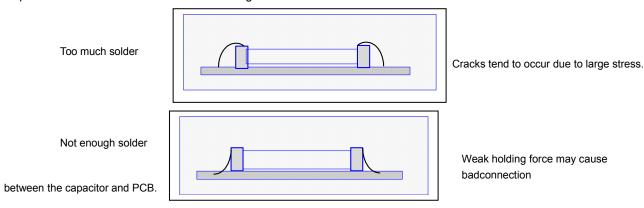
* Soldering Profile

To avoid the crack problem by sudden temperature change, follow the temperature profile in the adjacent graph (refer to the graph in the enclosure page).

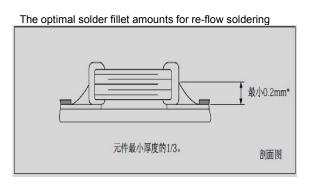
Manual Soldering

Manual soldering can pose a great risk of creating thermal cracks in capacitors. The hot soldering iron tip comes into direct contact with the end terminations, and operator's careless may cause the tip of the soldering iron to come into direct contact with the ceramic body of the capacitor. Therefore the soldering iron must be handled carefully, and pay much attention to the selection of the soldering iron tip and temperature contact of the tip.

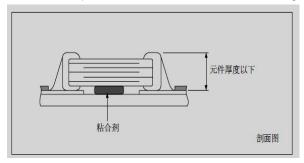
*Optimum Solder Amount for Reflow Soldering



* Recommended Soldering amounts

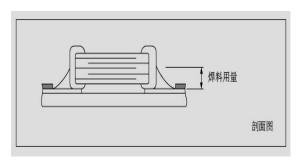


The optimal solder fillet amounts for wave soldering



The optimal solder fillet amounts for reworking by using soldering iron





* Recommended Soldering Method

Size	Temperature Characteristics	RatedVoltage	Capacitance	Soldering Method
0603	C0G	/	/	R
0805	C0G	1	1	R
0505	C0G	1	1	R
1111	C0G	1	1	R

Soldering method: Reflow Solering Wave Soldering

♦ The temperature profile for soldering

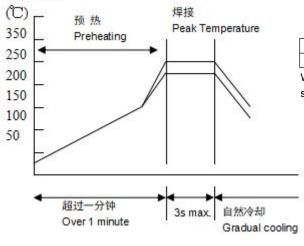
* Re-flow soldering

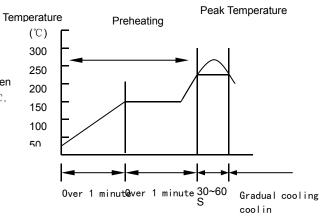
	Pb-Sn soldering	Lead-free soldering
Peak temperature	230℃~250℃	240℃~260℃

While in preheating,please keep the temperature difference between soldering temperature and surface temperature of chips as: T≤150°C.

* Wave soldering





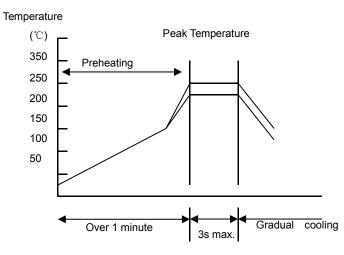


	Pb-Sn soldering	Lead-free soldering
Peak temperature	230℃~260℃	240℃~270℃

While in preheating, please keep the temperature difference between soldering temperature and surface temperature of chips as: $T \le 150^{\circ}$ C.



* Hand soldering



Conditions:

Containone.								
Preheating	Temperature of soldering iron head	Power of soldering iron	Diameter of soldering iron head	Soldering time	Solder paste amount	Restricted conditions		
∆≤130°C	Highest temperature:35 0°C	20W at the highest	1mm recommended	3s at the longest	≤1/2 chip thickness	Please avoid the derect contact between soldering iron head and ceramic components		